

Anuradha Engineering College, Chikhli
Department of Electronics & Telecommunication Engineering
Experiment List

Subject (Pr): 3ETp8 - ELECTRONIC DEVICES AND CIRCUITS – LAB Session-I (2020-2021)

Expt. No	Experiment List
Expt-1	To verify V-I characteristics of p-n junction diode and obtain static and dynamic resistance values.
Expt -2	To obtain the efficiency and ripple factor of half-wave, full-wave rectifiers without and with Filters (C-input filter).
Expt -3	To verify of Zener diode as a voltage regulator.
Expt -4	To verify characteristics of CE mode of BJT and compute its parameters.
Expt -5	To demonstrate voltage divider biasing of CE mode of BJT and determine operating point.
Expt -6	To Compute theoretical and practical frequency of oscillation of RC Phase shift and Hartley oscillators.
Expt -7	To obtain frequency response of RC coupled amplifier and verify cut off frequencies and bandwidth.
Expt -8	To Compute theoretical and practical gain and efficiency of class B / class AB power amplifier.
Expt -9	To plot the drain and transfer characteristics of JFET (Junction Field Effect Transistor) and calculate r_d , g_m and μ .
Expt -10	To plot the characteristics of UJT and to calculate the Intrinsic Stand-Off Ratio (η).

Mr. R. B. Phase
Subject Teacher